

DYNAMIC HEAD SWITCH TIMING RECALIBRATION
APPARATUS AND METHOD

Abstract of the Disclosure

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An apparatus and method for dynamic head switch timing recalibration are provided. With the apparatus and method, a determination is made as to whether a difference in a reference head switch timing and the head switch timing at a later time is sufficient to meet a certain criteria. If the difference meets the criteria, a recalibration of the head switch timing is performed. For example, in a preferred embodiment, the criteria is a number of address mark (AM) misses during a head switch operation. If this predetermined number of AM misses is detected, then it is determined that a recalibration is necessary since the predetermined number of AM misses is an indication that the head switch timing has changed significantly from the reference head switch timing.

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